



500.38899VX4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. YAMAMOTO
Serial No. 10/787,437
Filed: February 27, 2004
For: METHOD OF MANUFACTURING A DUAL GATE
SEMICONDUCTOR DEVICE WITH A POLY-METAL
ELECTRODE

CLAIM FOR FOREIGN PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 5, 2005

Sir:

Applicants hereby claim benefit under 35 USC §119 for Japanese Patent Application 2000-017875, filed in Japan on January 24, 2000. It is noted that a certified copy of the foreign application has been filed in the original parent application, Serial No. 09/639,306 on September 27, 2000.

Respectfully submitted,
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